

Annika 1.1 Block Diagram

01

PCB STACK UP

6L

LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : BOT

10.1" LCD Panel
Page 16

CRT
Page 12

Intel Pineview-M
Micro-FCBGA8
22 x 22 mm
TDP~5.5W
Page 3~5

DDR3 667MT/s
Single Channel

DDR3 SO-DIMM
2GB Max.
Page 11

XDP
Page 31

CLOCK GEN
9LRS3165
Page 2

SATA

2.5"HDD/SSD
Page 19

Intel Tigerpoint
17 x 17 mm
MMAP 360 Balls
TDP~1.5W
Page 6~10

USB 2.0

0, 1, 2
USB2.0 Port x3
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Card Reader
Page 13
Card Reader Socket
Page 13

5
Bluetooth/WLAN
Page 20

6
Touch Screen
Page 22

7
WWAN
Page 20
SIM Card Socket
Page 20

8
Webcam
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PCI-Express

X1
WWAN
Page 20
SIM Card
Page 20

X1
LAN
Realtek
RTL8103EL-VB
10/100
Page 15
RJ 45
Page 15

X1
WLAN
Page 20

X1
HD Decoder
Page 18
DDR II
64MByte
Page 18

SYSTEM POWER
+3VPCU/+5VPCU(RT8206)
PAGE 24

DDR 3 SMDDR_VTERM
+0.75VSMVREF/+1.5VSUS(RT8207)
PAGE 25

CPU CORE RT8152D
PAGE 26

SYSTEM CHARGER ISL6251AHAZ-T
PAGE 27

GFX CORE(RT9025)
+1.2V(RT9025)
+1.5V(RT9025)
PAGE 28
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PAGE 30

VCCP 1.05V(RT8209A)
PAGE 29

Touch Pad
Keyboard
Page 22

Power SW
Page 14

ENC KBC
KB3926D2
Page 23

BIOS
SPI Flash
Page 23

FAN
G991
Page 22

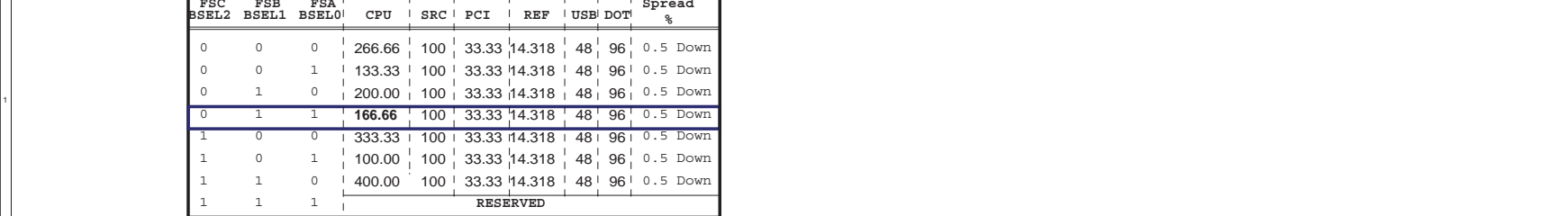
AUDIO CODEC
92HD80BX
Page 17

Int SPK
Page 17

Digital MIC
Page 17

HP/MIC
COMBO JACK
Page 17

<http://hobi-elektronika.net>



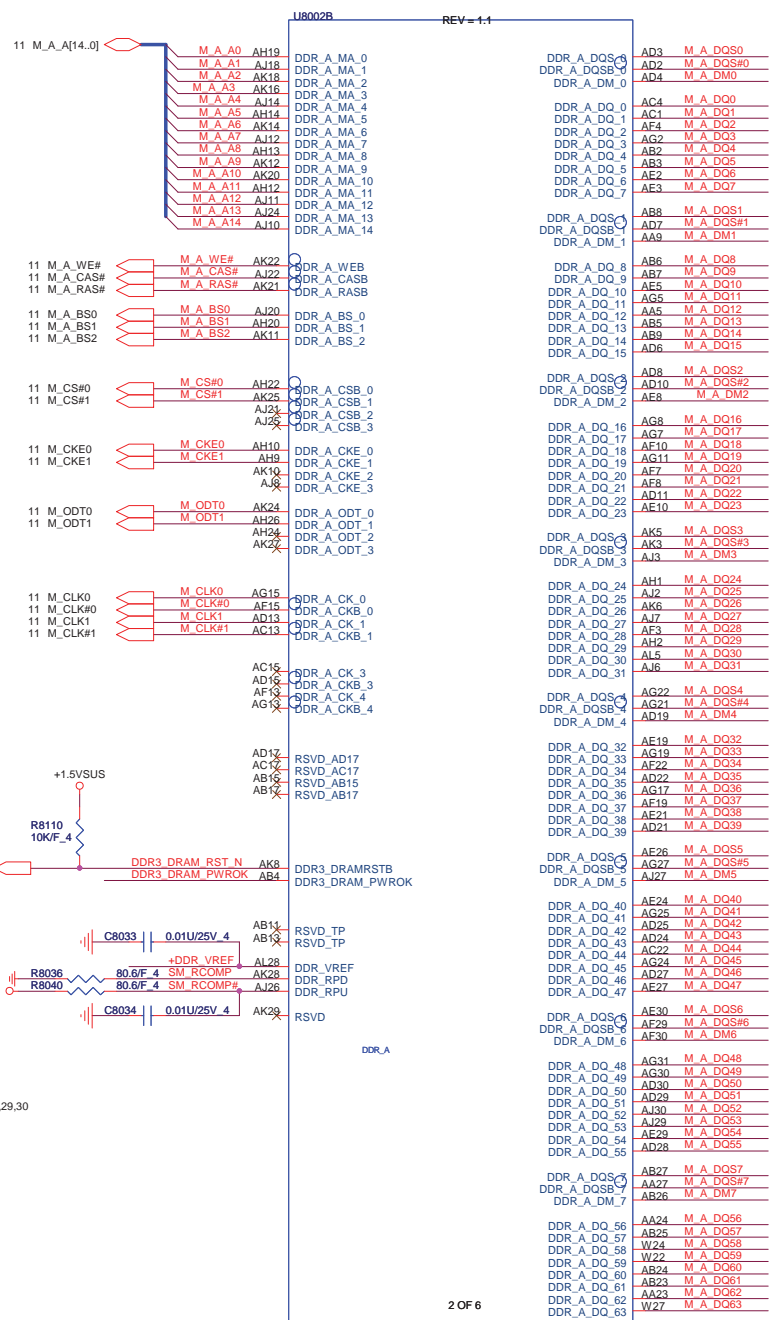
| | |
|---------------|------------|
| ITP_EN(PIN14) | PIN53/54 |
| 0 | SRC8#/SRC8 |
| * 1 | ITP/ITP# |

[illegible]

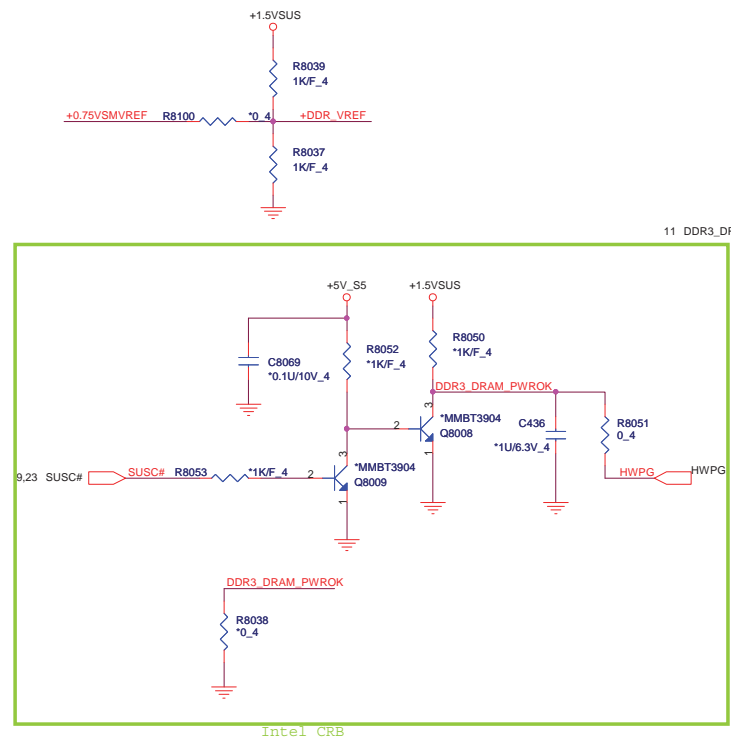


+1.5VSUS 5,11,25,28,29,30
+0.75VSMVREF 11,25

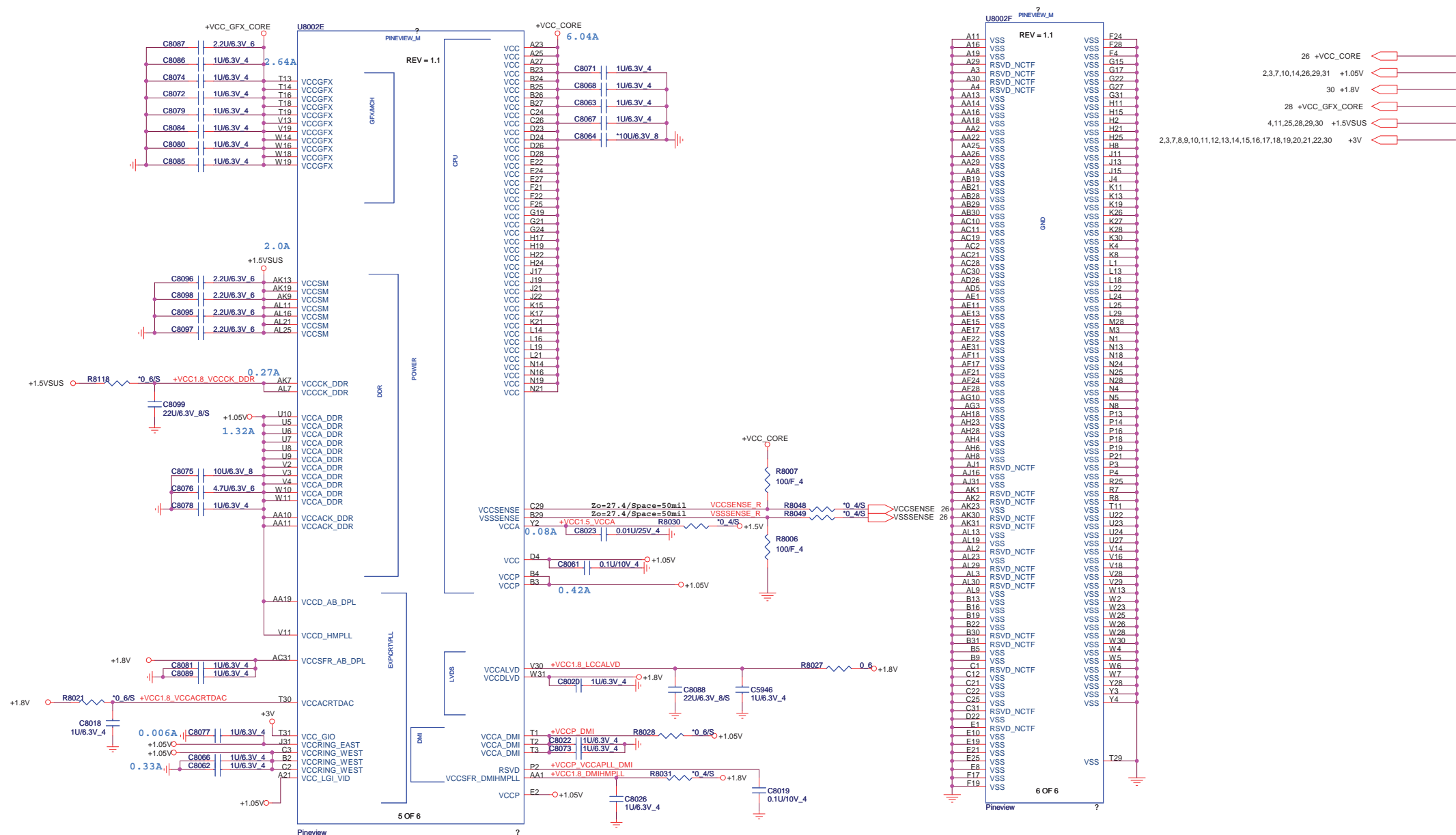
PINEVIEW.M

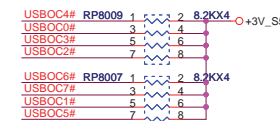


M_A_DQ[63..0] 11
M_A_DM[7..0] 11
M_A_DQS[7..0] 11
M_A_DQS#[7..0] 11

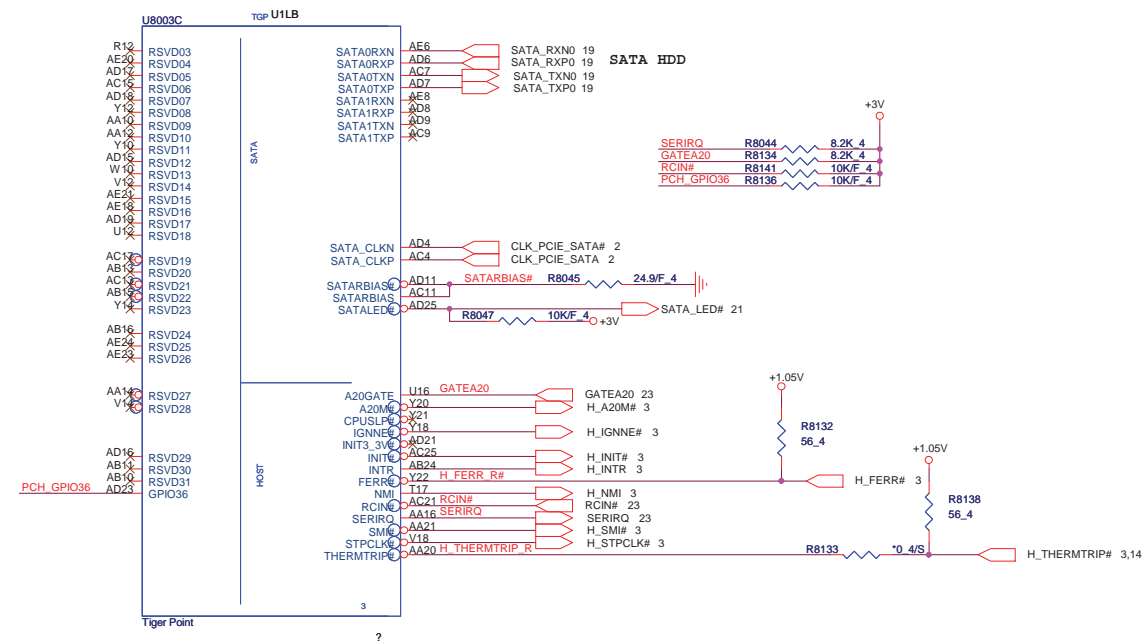


Intel CRB





+3V 2,3,5,8,9,10,11,12,13,14,15,16,17,18,19,20,21,22,30
+1.05V 2,3,5,10,14,26,29,31

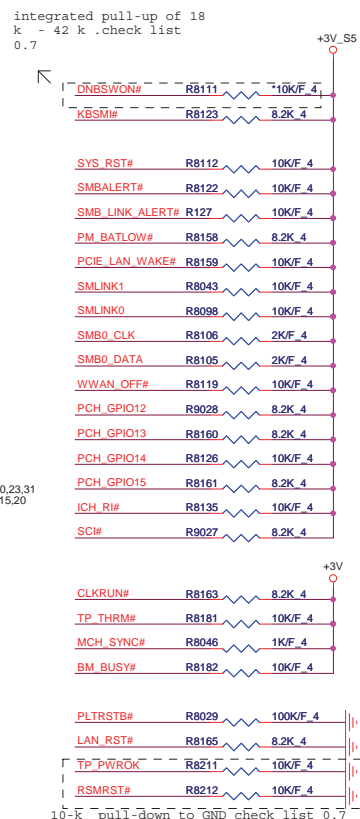




A16 SWAP Override strap

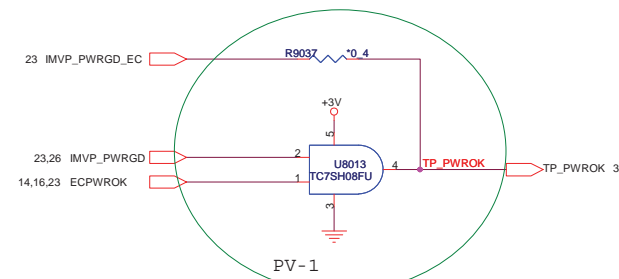
| IRQ | Description |
|--------------|--|
| PIRQA | USB UHCI Controller #1, #4 |
| PIRQB | AC'97 Codec; Option for SMBUS |
| PIRQC | USB UH Controller #3; SATA/IDE Native Mode |
| PIRQD | USB UHCI Controller #2 |
| PIRQE | Internal LAN; Option for SCI, TCO, HPET#0,1,2 |
| PIRQF | Option for SCI, TCO, HPET#0,1,2 |
| PIRQG | Option for SCI, TCO, HPET#0,1,2 |
| PIRQH | USB EHCI Controller; Option for SCI, TCO, HPET#0,1,2 |

| | |
|-----------|---------------------------------|
| PCI_GNT#2 | Internal PU Should not be PD |
|-----------|---------------------------------|

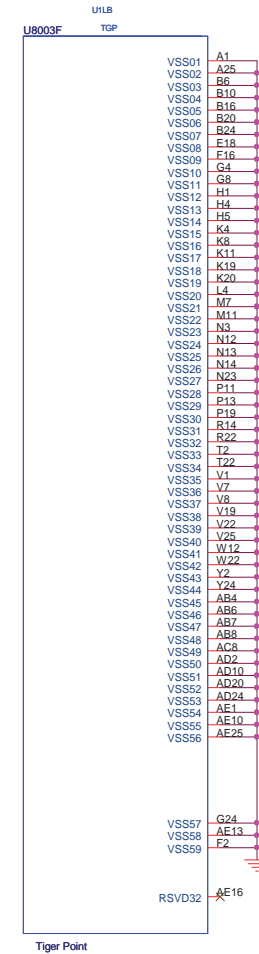
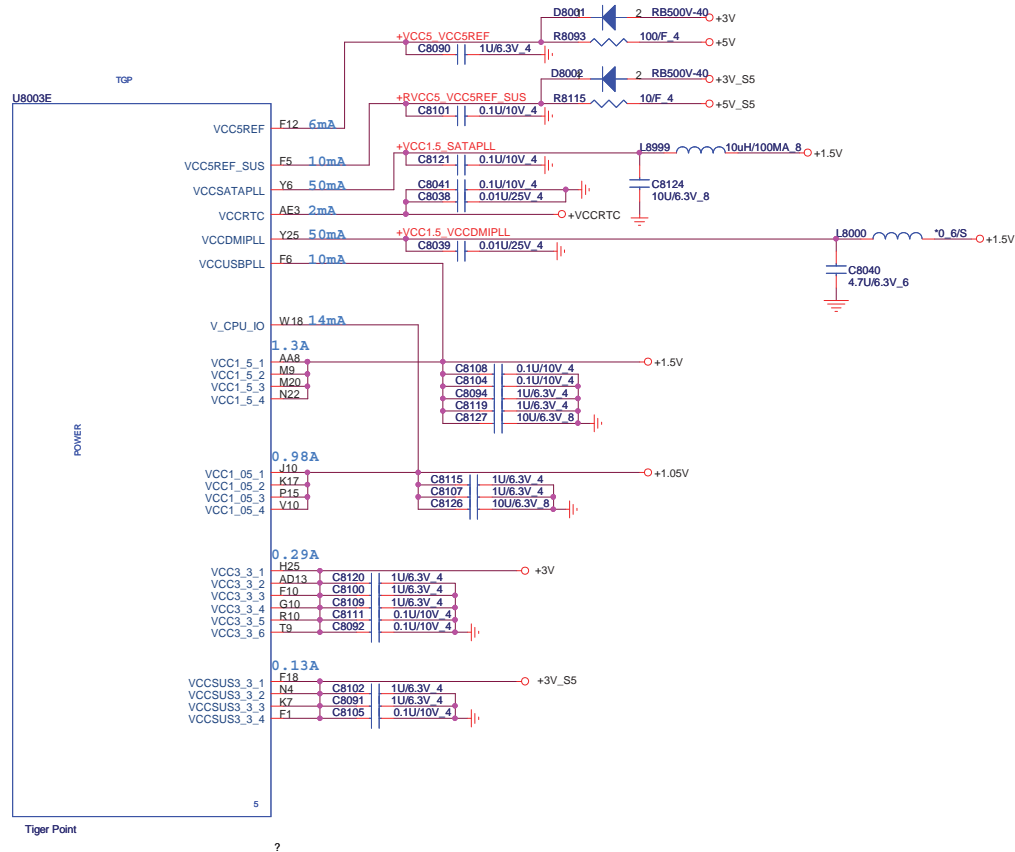


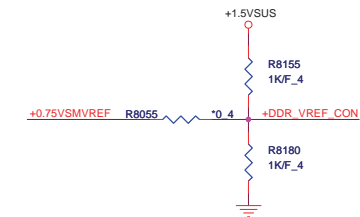
| | |
|---------------------|----------|
| | INTVRMEN |
| Enable (default) | 1 |
| Disable | 0 |


| ACZ_SDOUT (INT PD) | ACZ_SYNC (INT PD) | Description |
|-----------------------|----------------------|------------------------|
| 0 | 0 | ★ 4 x 1s |
| 1 | 0 | Reserved |
| 0 | 1 | Reserved |
| 1 | 1 | 1 x 4s(1 port/4 lanes) |



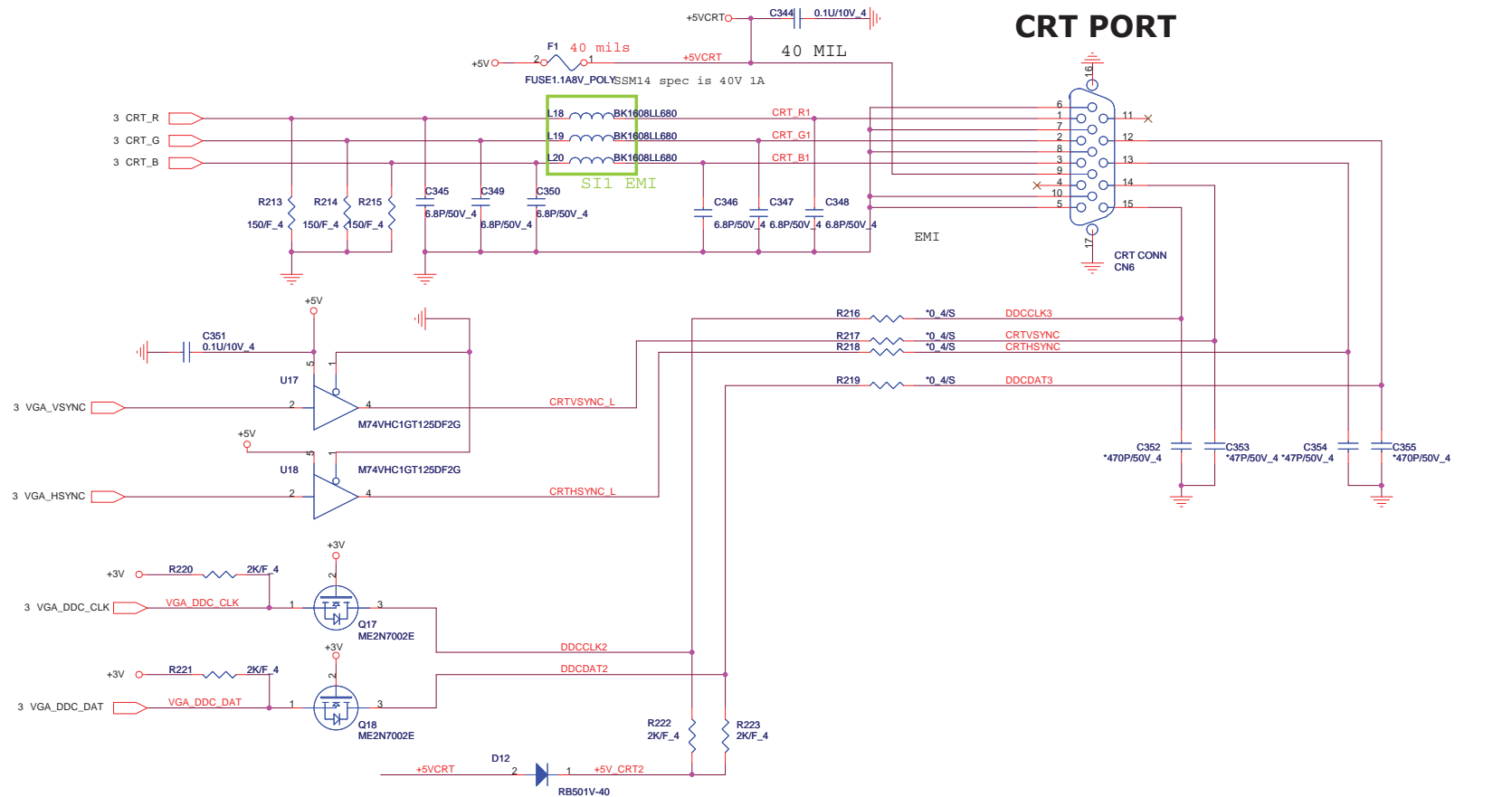
| | | |
|--|--------|--|
| 2,3,5,7,8,9,11,12,13,14,15,16,17,18,19,20,21,22,30 | +3V | |
| 12,16,17,19,20,22,30 | +5V | |
| 5,6,18,20,30 | +1.5V | |
| 2,3,5,7,14,26,29,31 | +1.05V | |
| 6,9,30 | +3V_S5 | |
| 4,30 | +5V_S5 | |

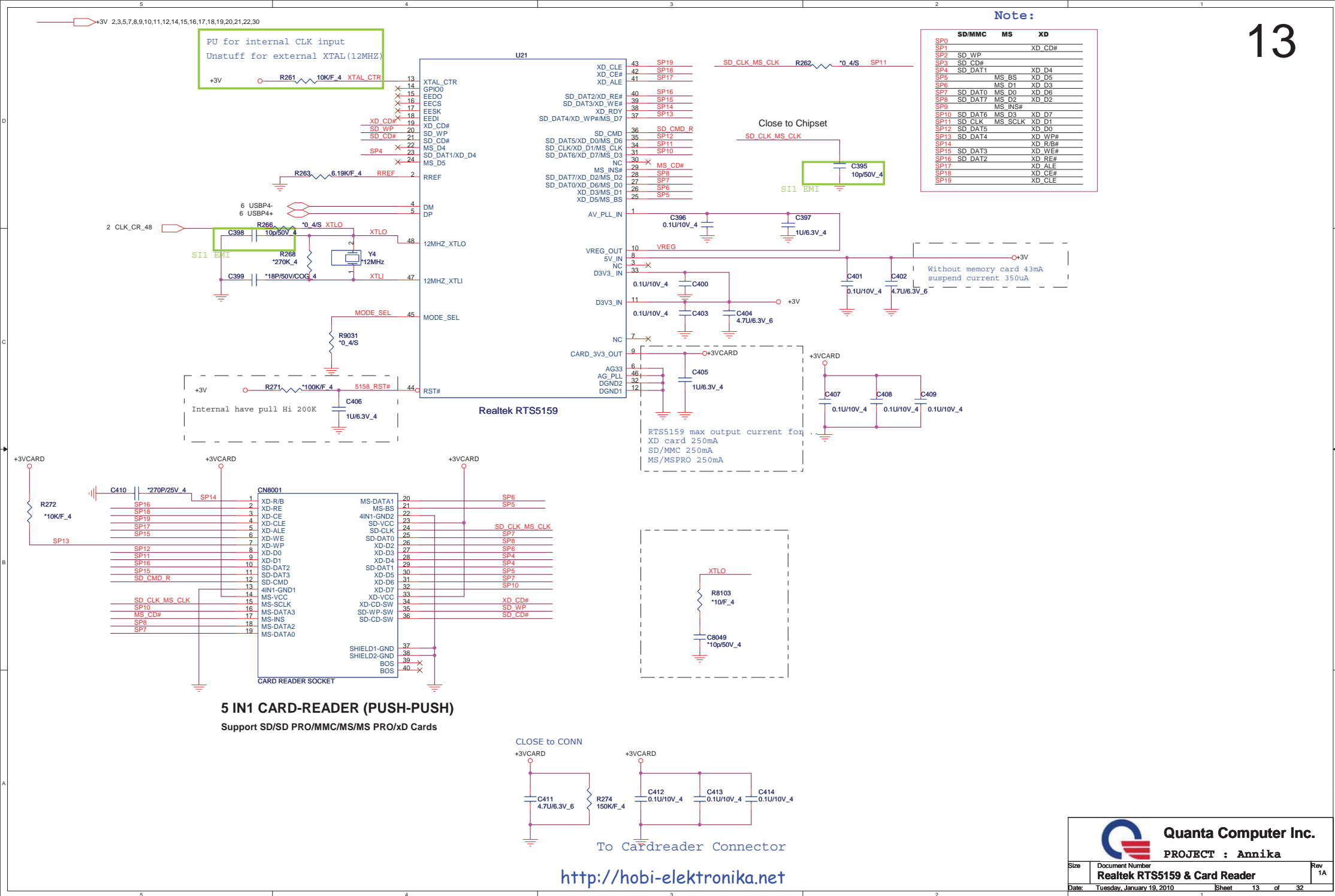


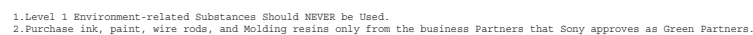


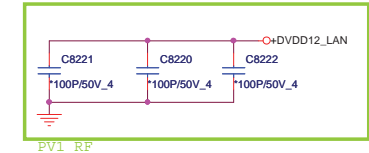
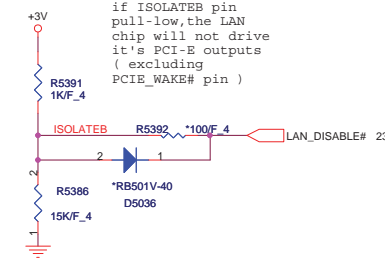
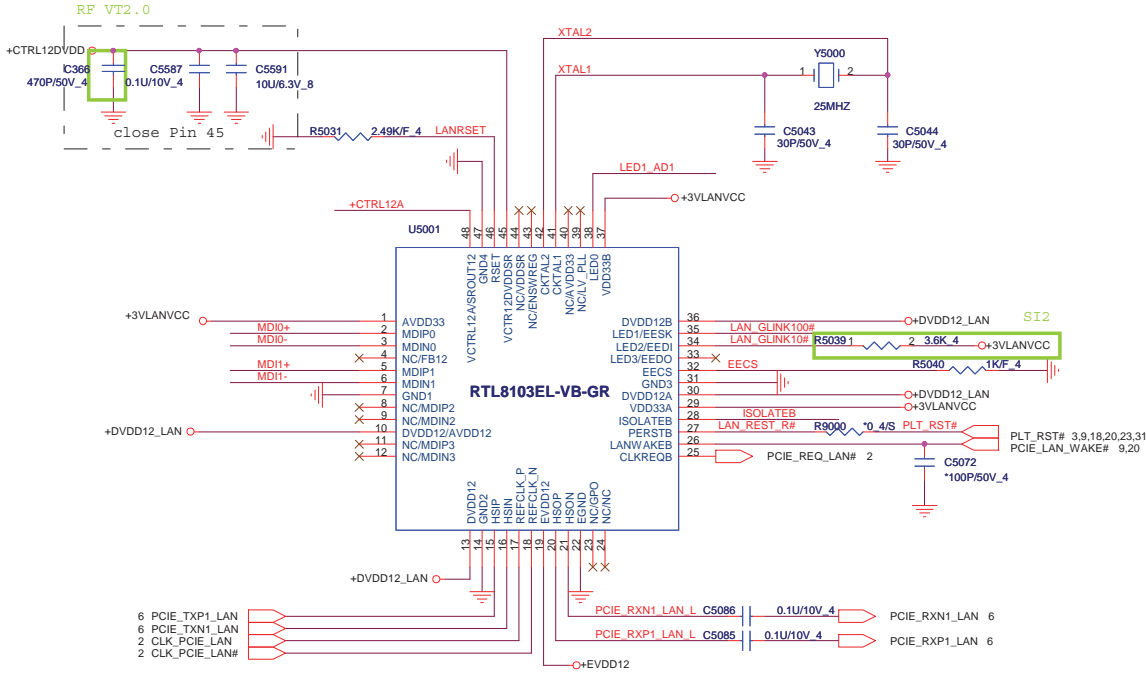
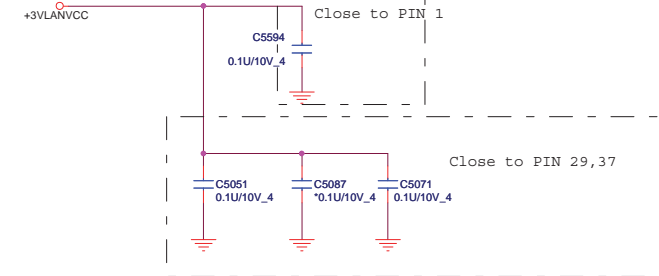
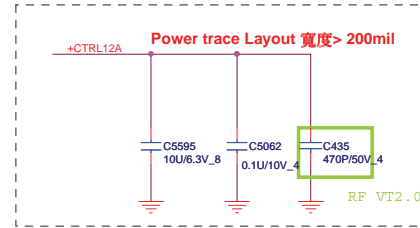
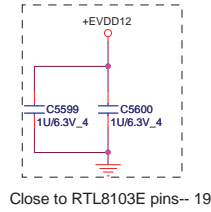
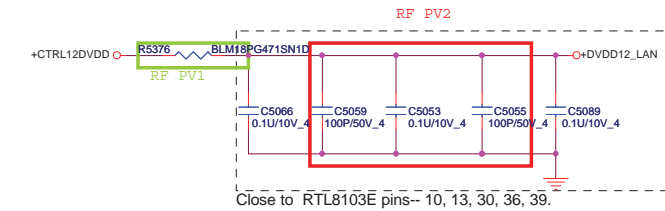
| | | |
|--|--|----------------|
|  Quanta Computer Inc. PROJECT : Annika | | Rev 1A |
| Size | Document Number DDRII SODIMM | |
| Date: 7 | Thursday, January 21, 2010 | Sheet 11 of 32 |

2,3,5,7,8,9,10,11,13,14,15,16,17,18,19,20,21,22,30 +3V
2,3,5,7,10,14,26,29,31 +1.05V

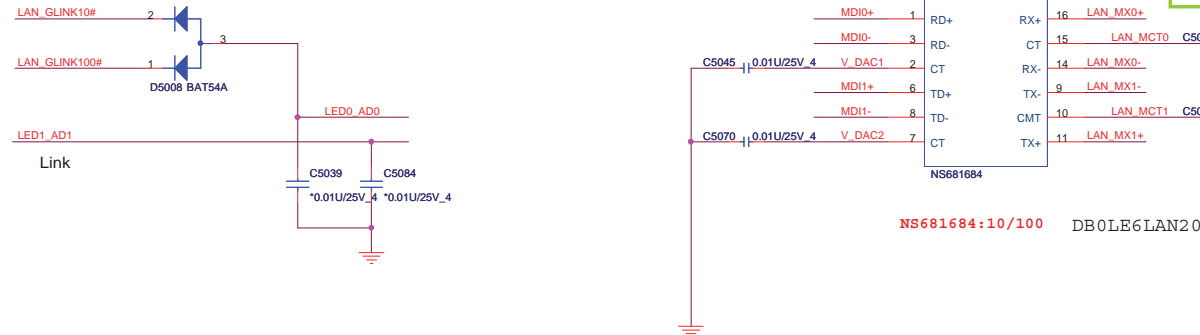
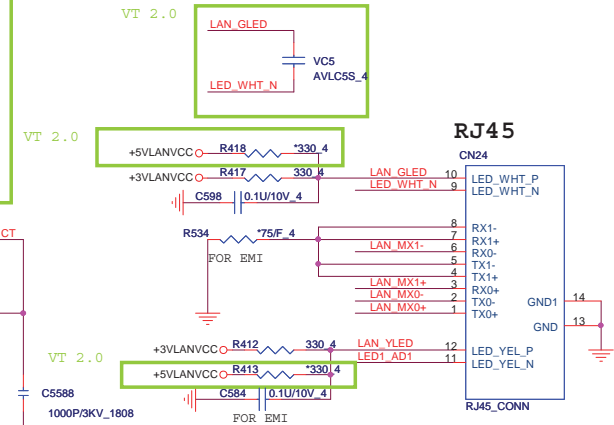
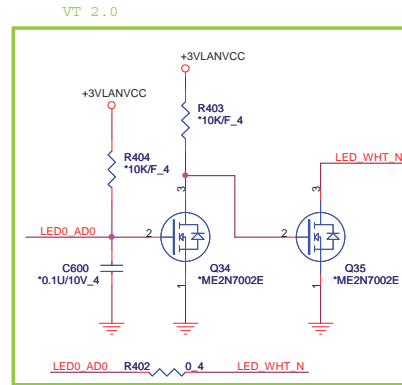






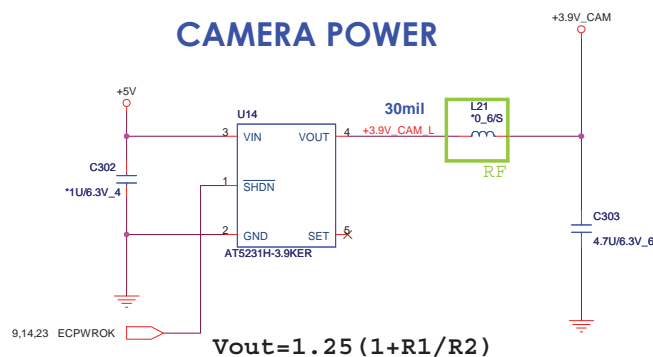
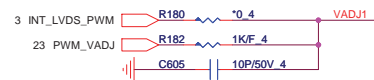
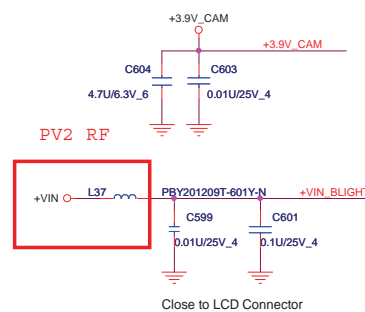
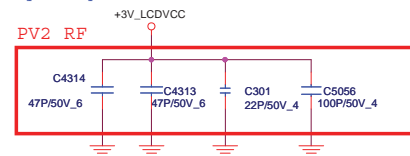
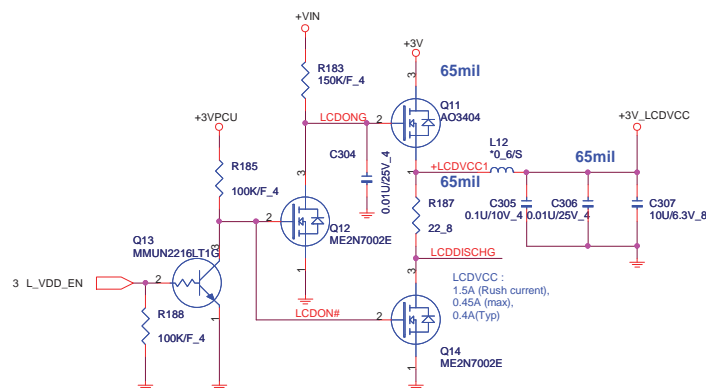
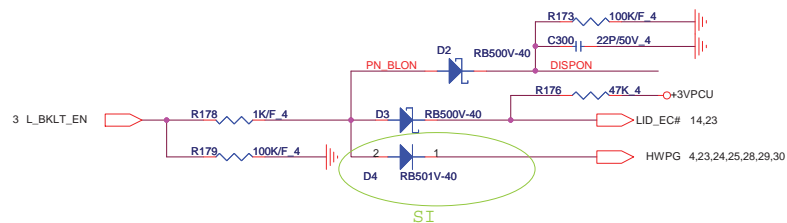


| Symbol | Type | Pin No (64-Pin) | Pin No (48-Pin) | Description |
|--------|------|-----------------|-----------------|---------------------------------|
| LED0 | O | 57 | 38 | LED0: 00 01 10 11 |
| LED1 | O | 56 | 35 | LED1: Tx/Rx Tx/Rx Tx Tx |
| LED2 | O | 55 | 34 | LED2: LINK100 LINK LINK LINK100 |
| LED3 | O | 54 | 33 | LED3: LINK10 FULL Rx LINK10 |
| | | | | LED3: NA NA NA NA |

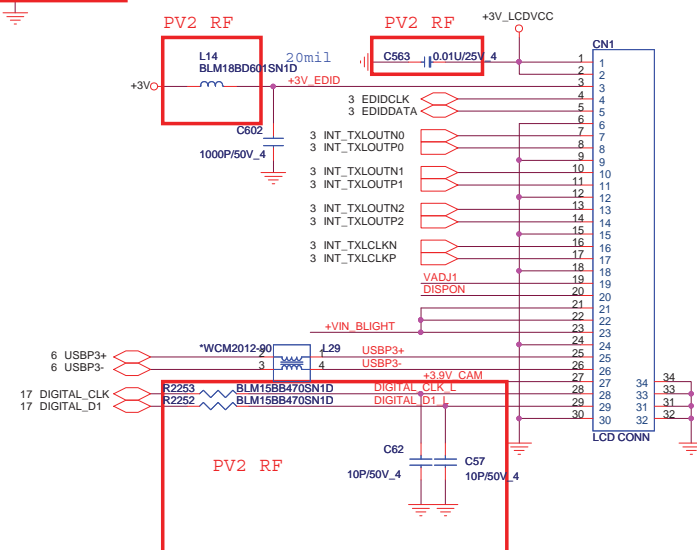


NS681684:10/100 DB0LE6LAN20

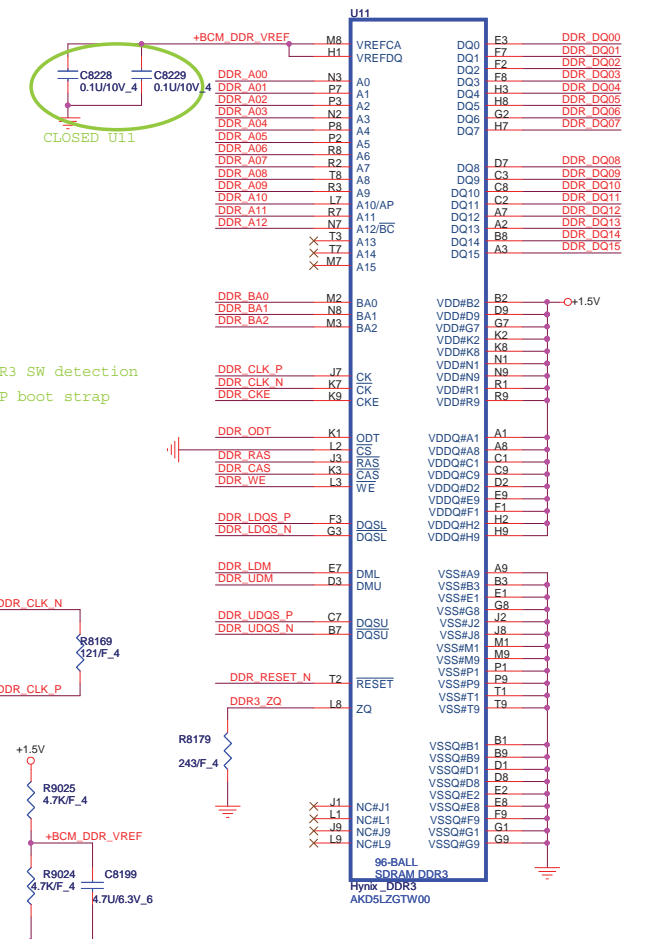
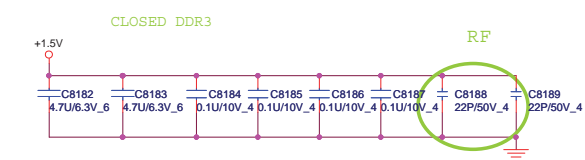
<http://hobi-elektronika.net>



$$V_{out} = 1.25 (1 + R_1/R_2)$$



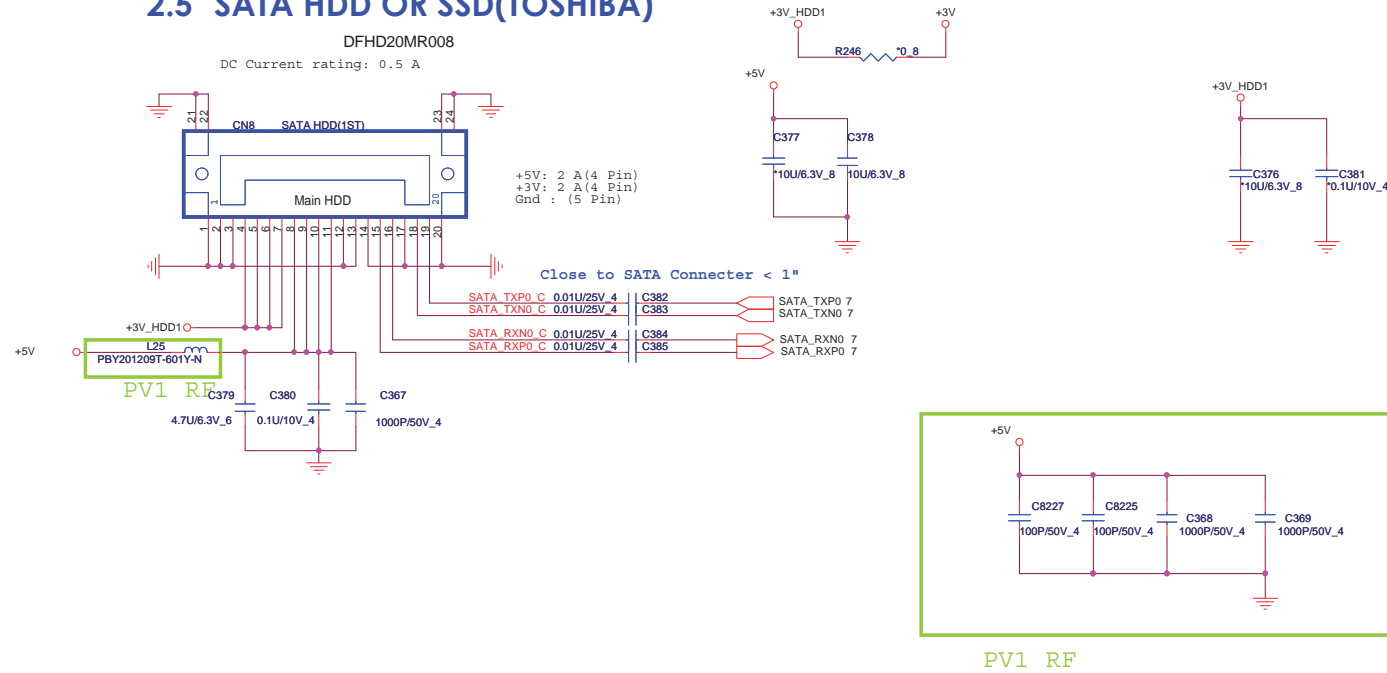




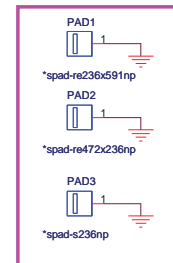
2.5" SATA HDD OR SSD(TOSHIBA)

DFHD20MR008

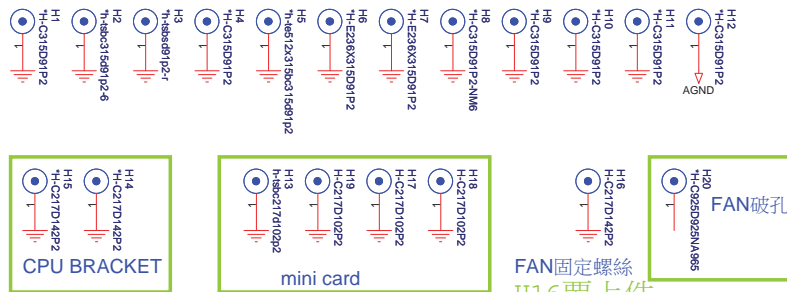
DC Current rating: 0.5 A



EMI spring



M/B Screw Hole



H13/17/18/19 要上件

FAN固定螺絲
H16要上件

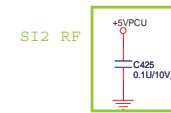
FAN破孔

固定孔

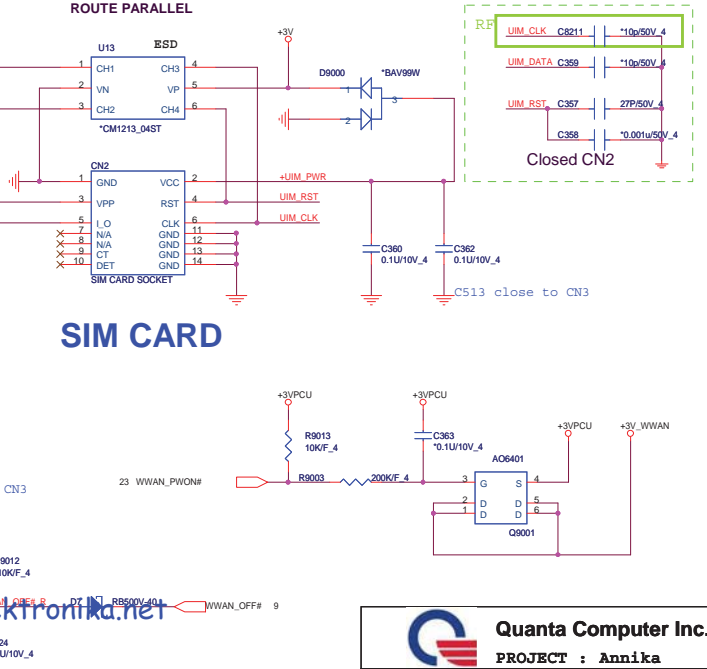
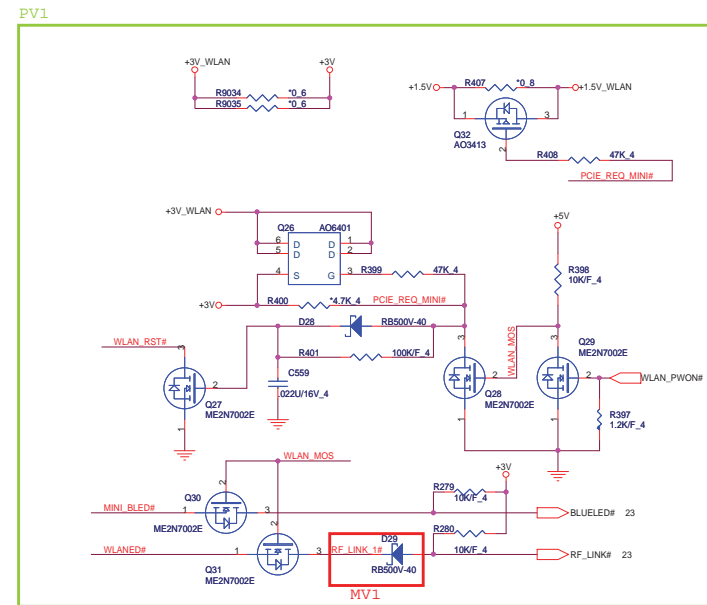
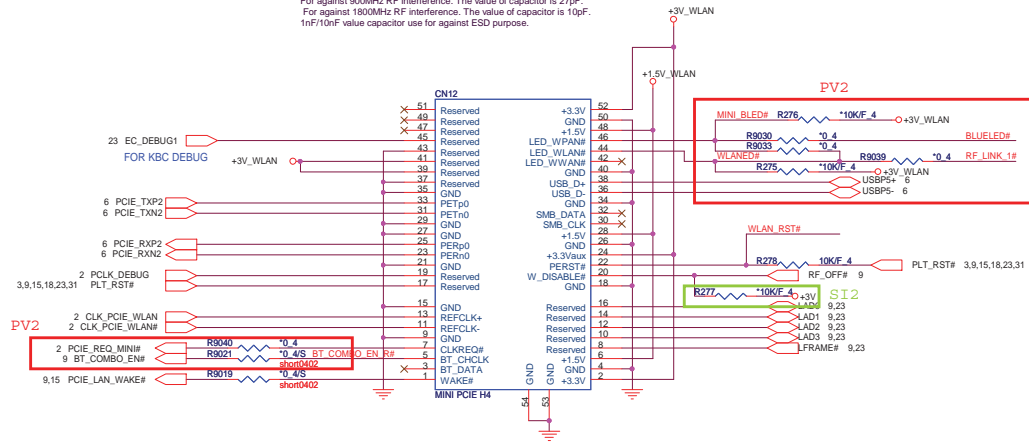


Quanta Computer Inc.

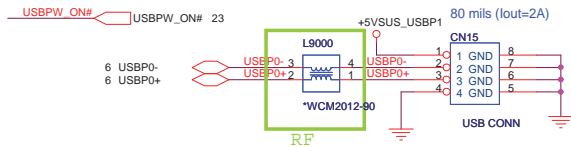
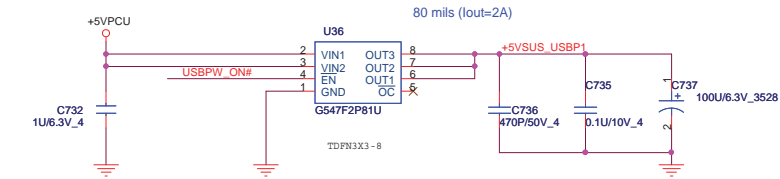
PROJECT : Annika



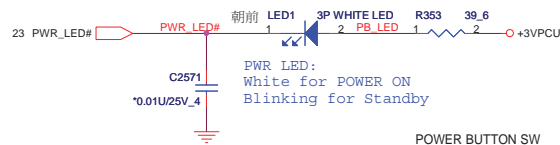
The value of the capacitor is suggest by Siemens HQ expert.
For against 900MHz RF interference. The value of capacitor is 27pF.
For against 1800MHz RF interference. The value of capacitor is 10pF
1nF/10nF value capacitor use for against ESD purpose.



1x Left side USB port supports Keyed USB.



PWR Button/LED

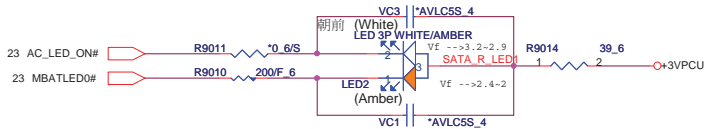


POWER BUTTON SW

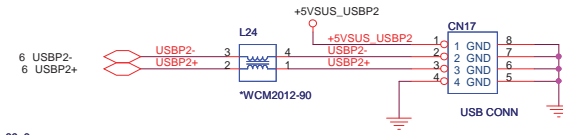
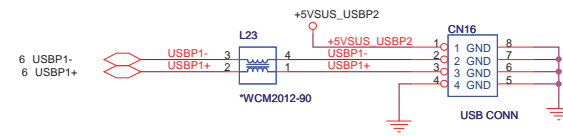
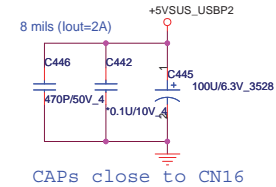
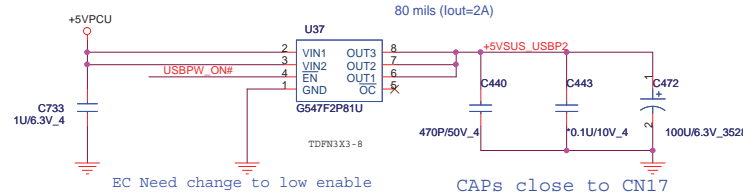
SATA/LED



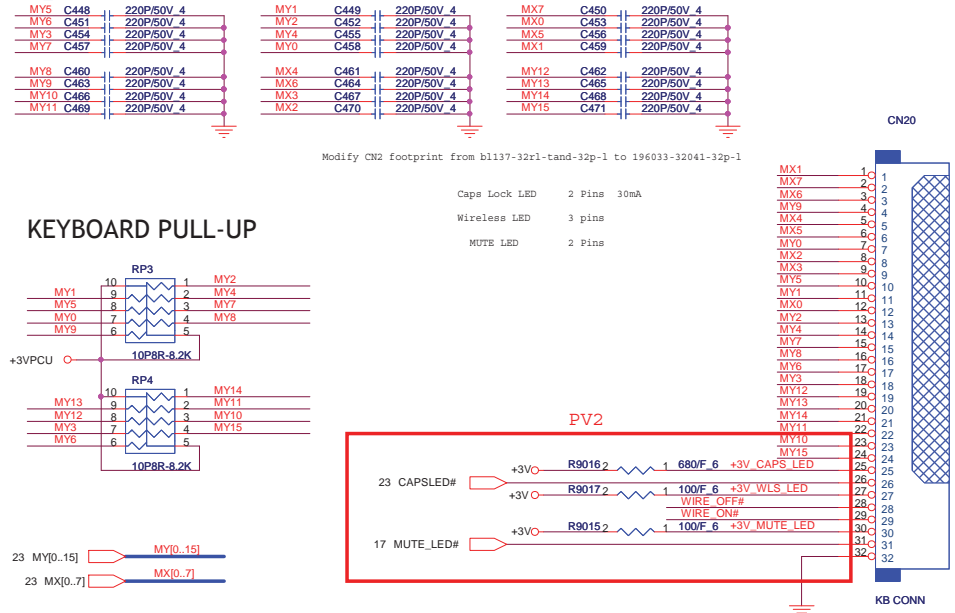
Charging & Discharging/LED



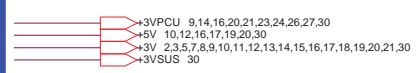
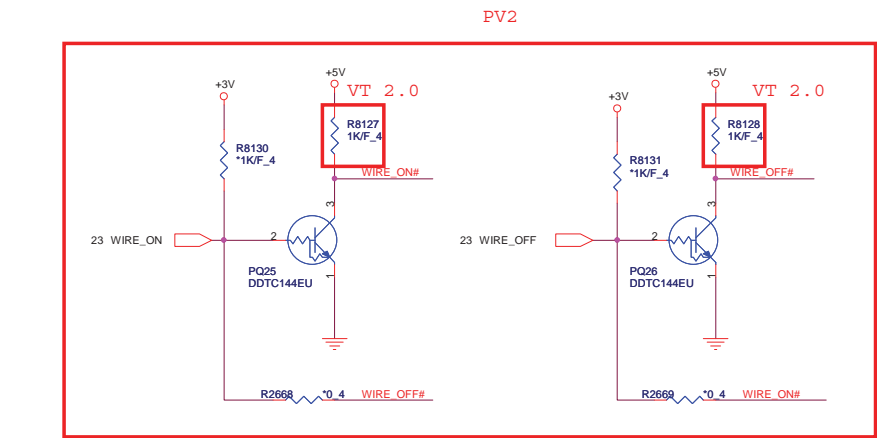
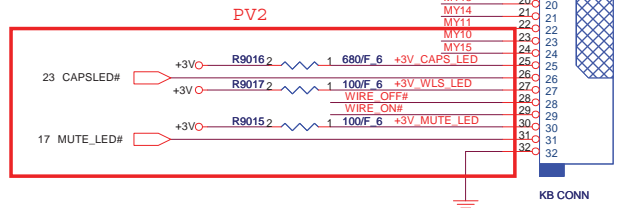
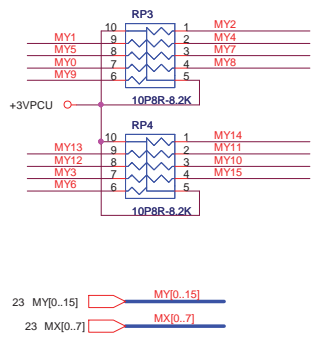
For Right 2xUSB Ports PWR



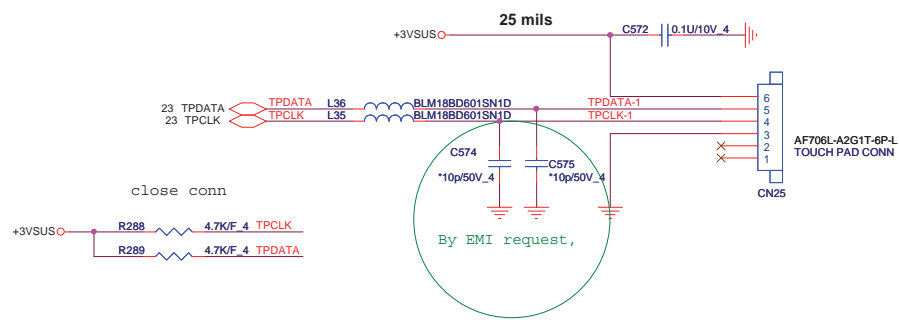
Keyboard (KBC)



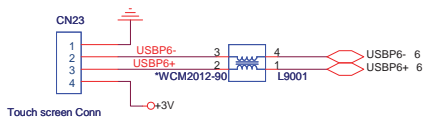
KEYBOARD PULL-UP



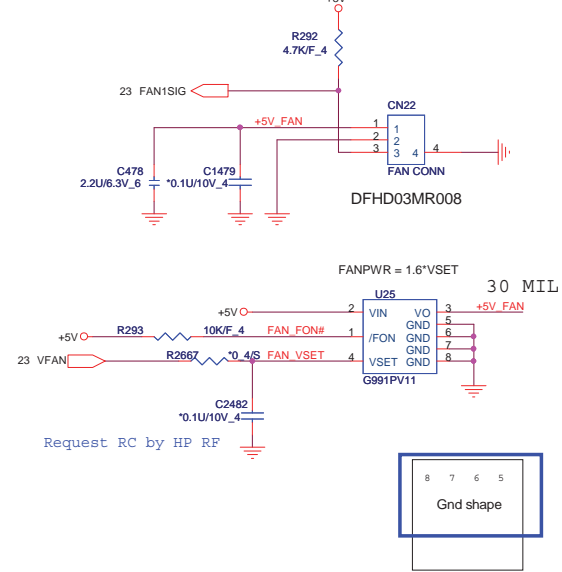
TOUCH PAD CONNECTOR



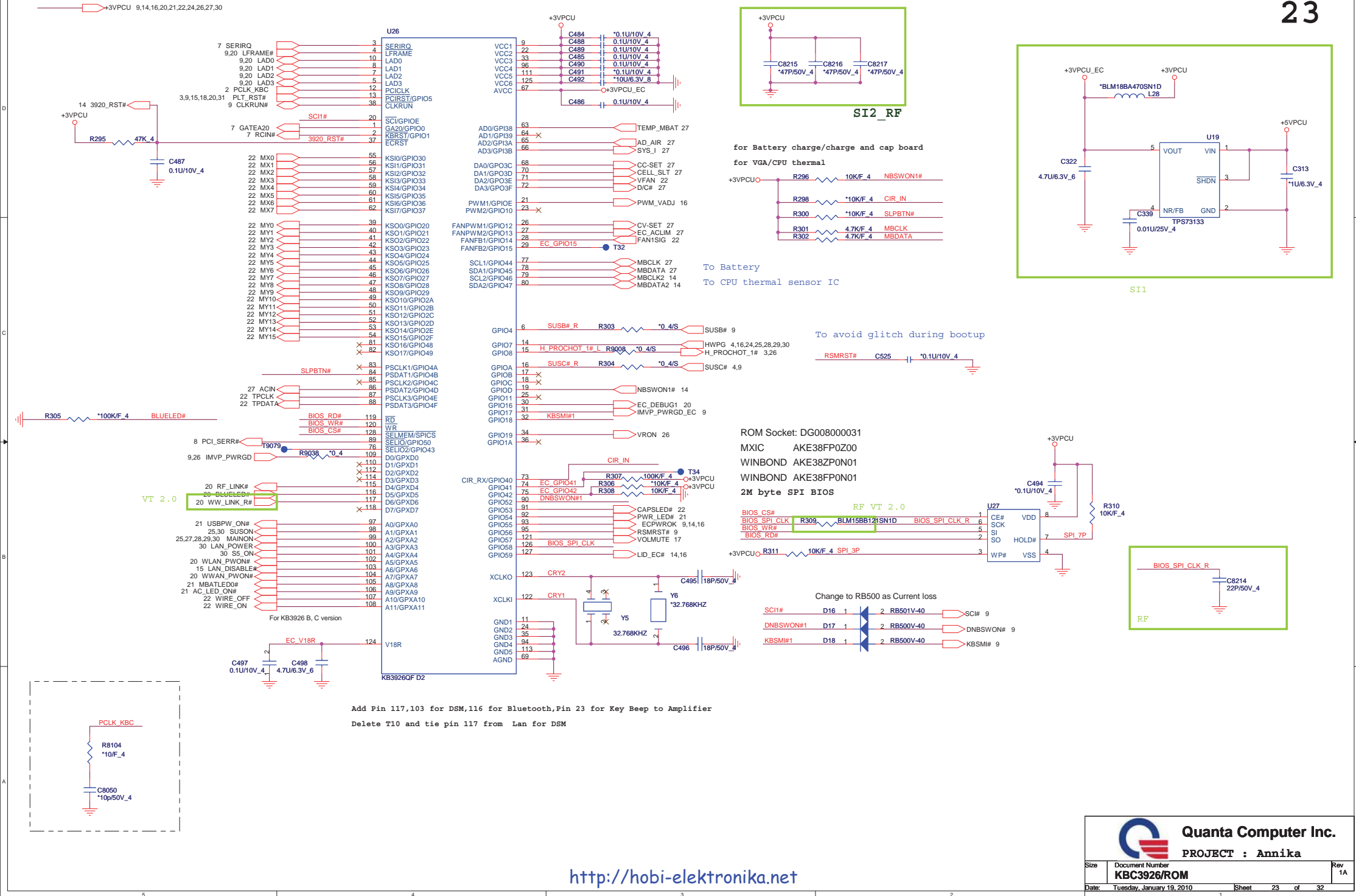
TOUCH SCREEN



CPU FAN



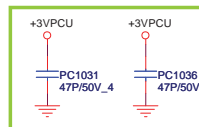
<http://hobi-elektronika.net>



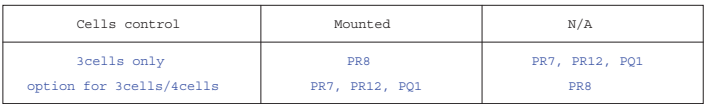
**Place these CAPs
close to FETs**

Place these CAPs
close to FETs

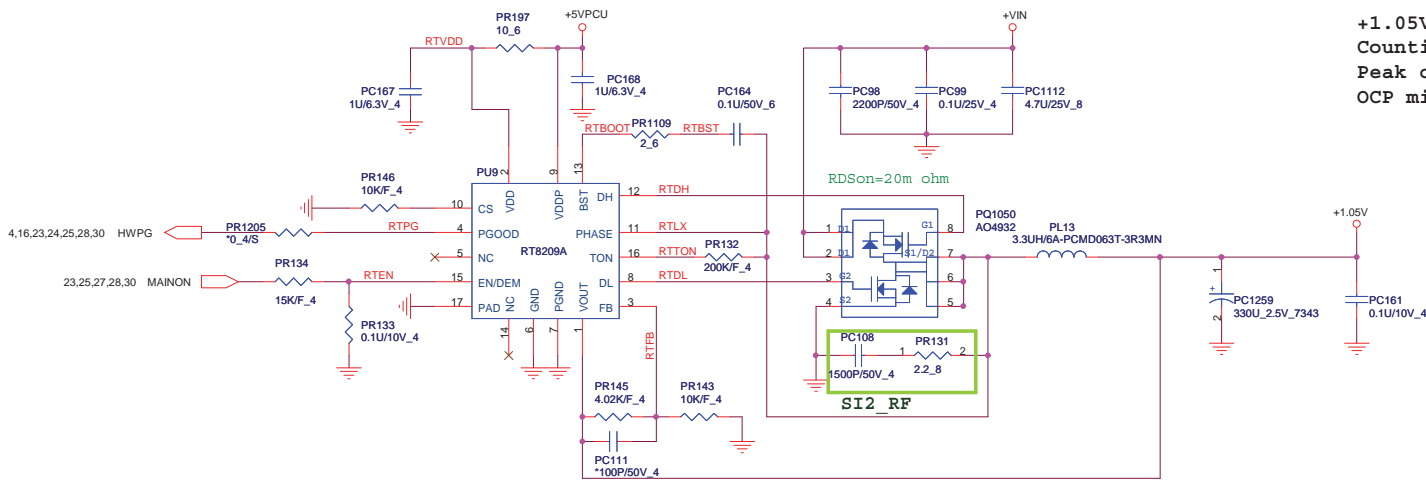
SI2 RF



SI1 RF

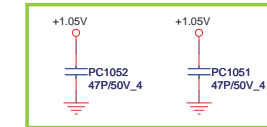




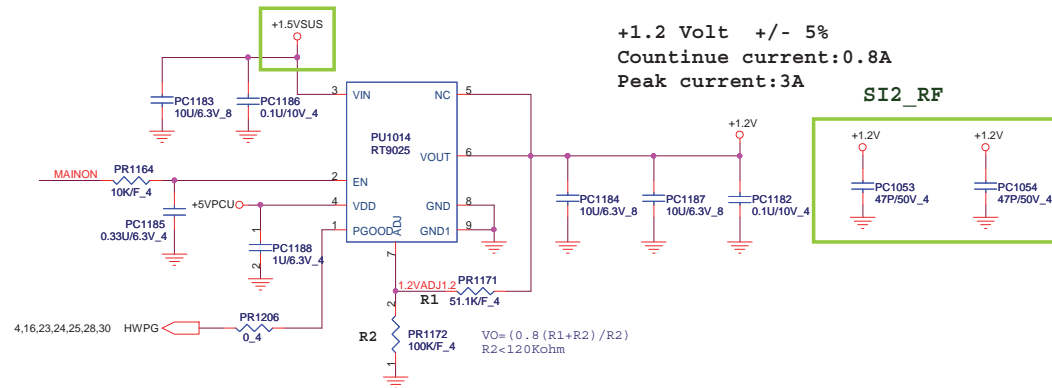
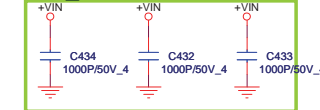


+1.05Volt +/- 5%
Countinue current:3A
Peak current:4A
OCP minimum:5A

SI2_RF

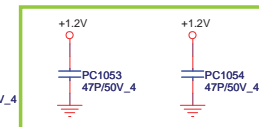


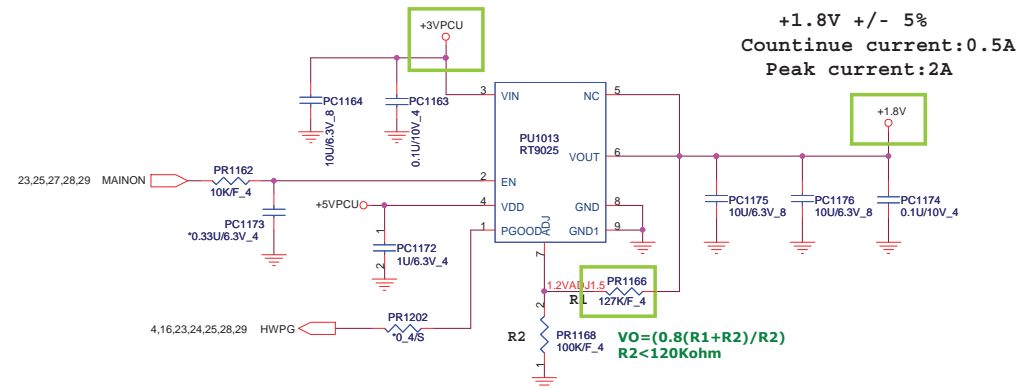
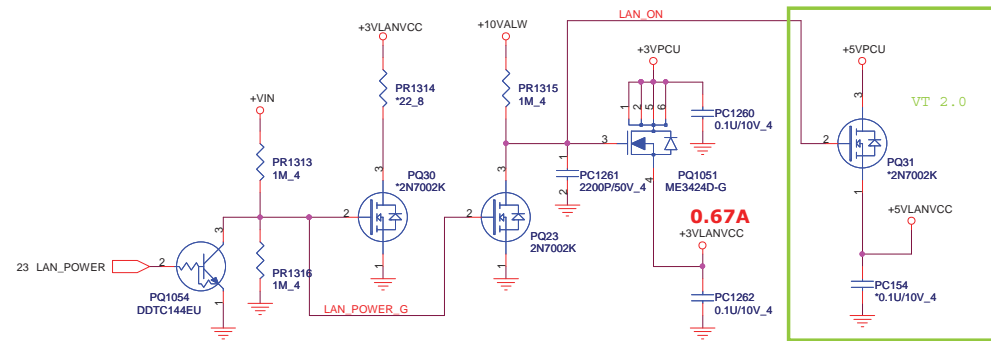
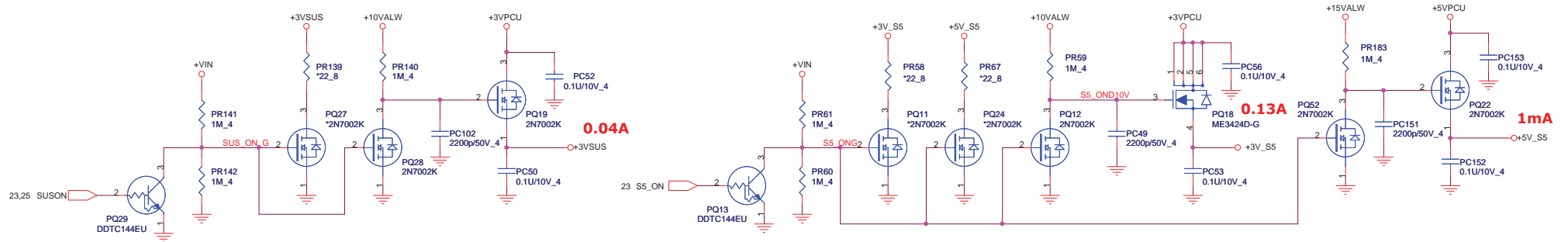
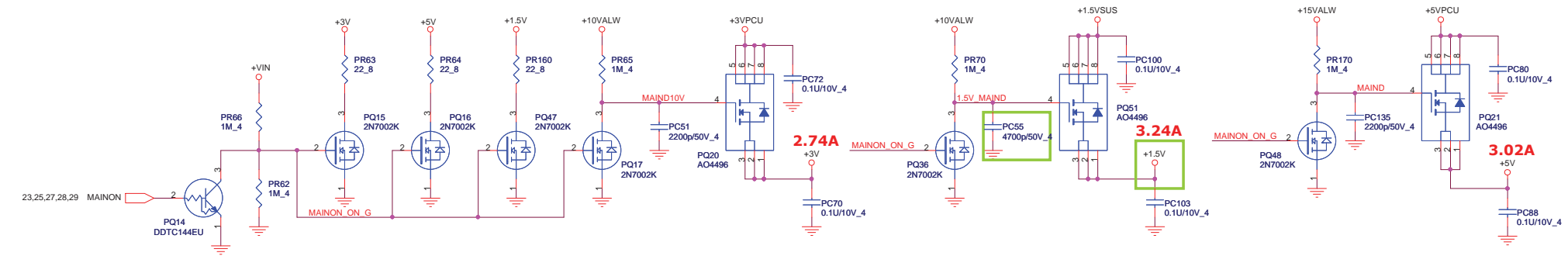
PV1_RF



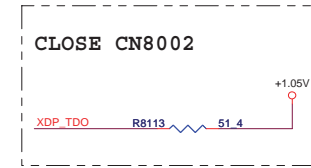
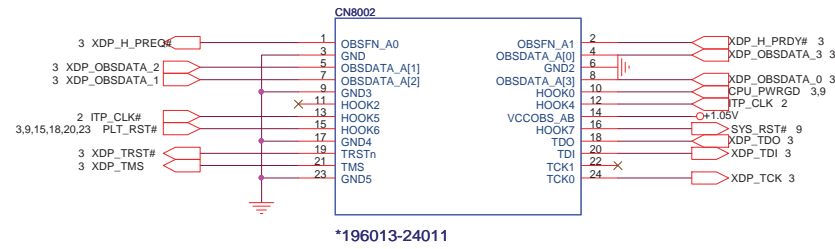
+1.2 Volt +/- 5%
Countinue current:0.8A
Peak current:3A

SI2_RF





CPU XDP



Power up sequence

